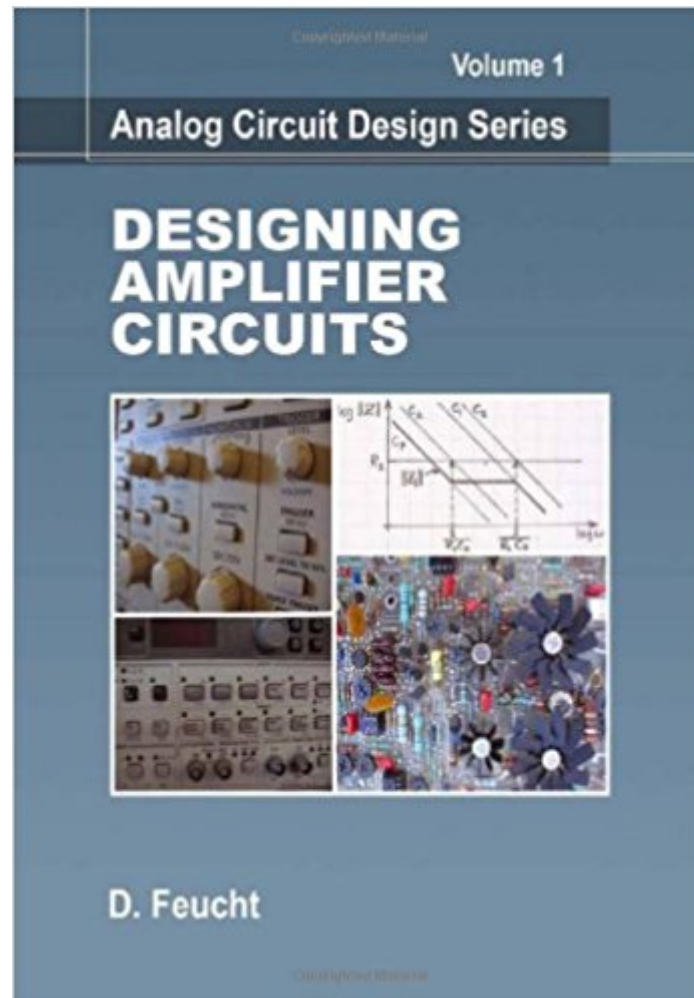


The book was found

Designing Amplifier Circuits (Analog Circuit Design)



Synopsis

The Analog Circuit Design Series set reduces the concepts of analog electronics to their simplest, most obvious form which can easily be applied (even quantitatively) with minimal effort. The emphasis of the set is to help you intuitively learn through inspection how circuits work and apply the same techniques to circuits of the same class. This first volume, *Designing Amplifier Circuits*, presents the basic principles of transistor circuit analysis, basic per-stage building blocks, and feedback. The content is restricted to quasi-static (low-frequency) considerations, to emphasize basic topological principles. The reader will be able to analyze and design multi-stage amplifiers with feedback, including calculation and specification of gain, input and output resistances, including the effects of transistor output resistance. Of note is the presentation of feedback analysis, a subject rarely covered by other books, with insights and from angles that will reduce to analysis by inspection for readers. Some circuit transformations outlined within are especially helpful in reducing circuits to simpler forms for analysis. They are usefully applied in considering transistor circuits for which collector-emitter (or drain-source) resistance is not negligible, another often omitted topic which this book details.

Book Information

Series: Analog Circuit Design (Book 1)

Paperback: 207 pages

Publisher: SciTech Publishing (December 2, 2010)

Language: English

ISBN-10: 1891121863

ISBN-13: 978-1891121869

Product Dimensions: 5.9 x 0.6 x 8.8 inches

Shipping Weight: 12.8 ounces (View shipping rates and policies)

Average Customer Review: 3.9 out of 5 stars 2 customer reviews

Best Sellers Rank: #3,423,411 in Books (See Top 100 in Books) #84 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Electronics > Solid State](#) #1007 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design](#) #22804 in [Books > Science & Math > Technology](#)

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Key Features: - Little known circuits and techniques are revealed that can improve your circuit design and analysis skills. - Explains fast, accurate, and simple circuit methods. - Simulators will not

create your circuits: this shows how. - Graphically-driven presentation of concepts; like a series of seminars. - Written by 30 year veteran designer. --This text refers to an alternate Paperback edition.

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There is some merit to this series. Unfortunately it is hard to follow some of the analysis, and it will leave the inexperienced young electronics professional unable to follow....I have a fair bit of experience, and I find certain sections hard to follow. Another thing is there are examples of bad amplifier analysis or circuits that just won't work. Two examples: Botched amplifier noise analysis. ON page 95, in Volume 3, under his example of opamp input noise he has a completely botched analysis of the output noise. He doesn't give an equation (see what I mean?), but it's not too hard to figure out what he is doing. His equation (not given, but intuited from data given): $e_{no} = \sqrt{e_{ni}^2 + 2(e_n R)^2 + (i_n R_{fb})^2} = 61.1 \text{ nV}/\sqrt{\text{Hz}}$ All Wrong! The proper equation: $e_{no} = \sqrt{((1 + R_{fb}/r_{in})e_{ni})^2 + (i_n R_{fb})^2 + 2(e_n R)^2} = 70.8 \text{ nV}/\sqrt{\text{Hz}}$ The only reason he ends up somewhat close to the correct answer is because the 100k resistors used in his example comprise the dominant source of noise. Its obvious if you are in an inverting gain of 1 that the amplifier itself is in a noise gain of 2, which means you have to at least get 2x the input referred voltage noise at the output..... $(1 + R_{fb}/r_{in})e_{ni}$ or $40 \text{ nV}/\sqrt{\text{Hz}}$. The resistors contribute the $\sqrt{2} \cdot 40.7 \text{ nV}/\sqrt{\text{Hz}}$...or $57.55 \text{ nV}/\sqrt{\text{Hz}}$the current noise is only gained up to the output by the feedback resistor $i_n R_{fb} = 10 \text{ nV}/\sqrt{\text{Hz}}$. Rss those together you get the $70.8 \text{ nV}/\sqrt{\text{Hz}}$. Another example of a circuit that will not work is the one on page 125, which has positive feedback, and would latch at one supply rail or the other depending on amplifier offset.

For any person wanting to learn the fine details of analog solid state design the series of books by Feucht is excellent. He is an experienced and competent engineer who handles his subject clearly and professionally. His background as a vertical amplifier designer at Tektronix gives him a solid foundation for this book on amplifier design. Here transistor modelling is covered in detail and several working circuits are given and can be checked with SPICE or by construction. Young engineers in training will benefit from the foundational approach used. Those who are accustomed mainly to using opamps as building blocks will learn the fundamentals of discrete transistor

multistage amplifier design. As a seasoned analog designer myself, I recommend the book highly.

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